



# Hermeticity Testing of MEMS and Microelectronic Packages (Integrated Microsystems)

*By Suzanna Costello, Marc P y Desmulliez*

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Packaging of microelectronics has been developing since the invention of the transistor in 1947. The authors cover the history and development of packaging, along with a view to understanding initial hermeticity testing requirements and the subsequent limitations of these methods when applied to new package types.

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#### About the Author

Marc P.Y. Desmulliez is the head of the research institute in sensors, signals, and systems at Heriot-Watt University. He earned his Ph.D. in electrical engineering from Heriot-Watt University. Suzanne Costello is a development scientist at MCS Ltd. She earned her Ph.D. in engineering from Heriot-Watt University.

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